

10/580827
IAP9 Rec'd PCT/PTO 26 MAY 2006

Docket No.: R2184.0501/P501
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Masanori Dainin

Application No.: Not Yet Assigned	Confirmation No.: N/A
Filed: Concurrently Herewith	Art Unit: N/A
For: SEMICONDUCTOR DEVICE AND FABRICATION PROCESS THEREOF	Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

MS PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

A summary/abstract translation of the non-English language references is enclosed.

Applicant has not submitted copies of each cited U.S. patent and U.S. patent application as required by 37 CFR 1.98(a)(2)(i), amended October 2004, as the U.S. Patent and Trademark Office has waived this requirement for all U.S. patent applications. Applicant submits herewith copies of foreign and non-patents in accordance with 37 CFR 1.98(a)(2).

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper

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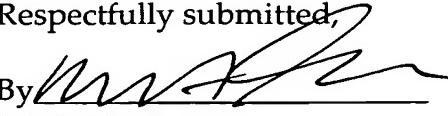
Docket No.: R2184.0501/P501

hereafter filed in this application by this firm) to our Deposit Account No. 04-1073,
under Order No. R2184.0501/P501. A duplicate copy of this paper is enclosed.

Dated: May 26, 2006

Respectfully submitted,

By


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PTO/SB/08a/b (07-05)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449A/B/PTO				<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	Not Yet Assigned
<i>(Use as many sheets as necessary)</i>				Filing Date	Concurrently Herewith
				First Named Inventor	Masanori Dainin
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	
R2184.0501/P501					

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			
BA		EP-1310998-A2	05-14-2003		
BB		JP-9-17860-A	01-17-1997		
BC		JP-2000-340562	12-08-2000		
BD		JP-2001-284464-A	10-12-2001		
BE		JP-6-112410-A	04-22-1994		
BF		JP-3195828-B2	06-01-2001		
BG		JP-2003-152100-A	05-23-2003		
BH		JP-2004-146548-A	05-20-2004		

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NON PATENT LITERATURE DOCUMENTS						T ²	
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
CA		LIFSHITZ, Nadia, "Dependence of the Work-Function Difference Between the Polycilicon Gate and Silicon Substrate on the Doping Level in Polysilicon", IEEE Transactions on Electron Devices, vol. ed. 32, No. 3, March 1985					

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Examiner Signature	Date Considered
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